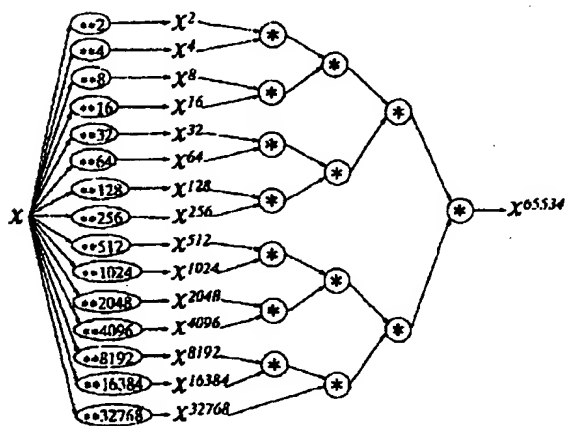


Example calculation process using Fermat's little theorem ( $m = 16$ )

Prior Art

Fig. 1

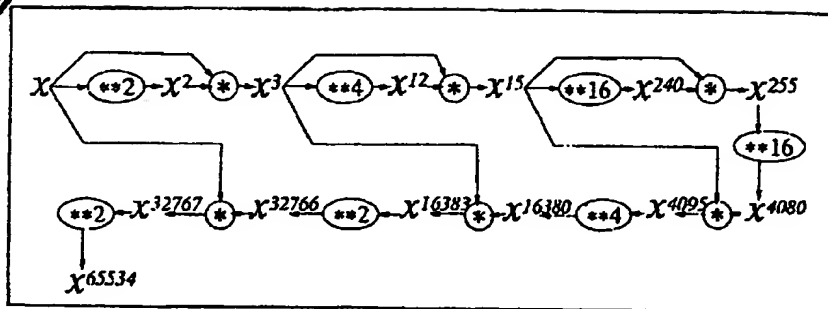
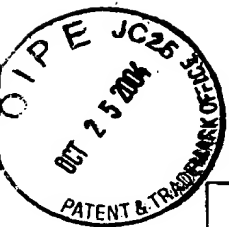


Example calculation process using Fermat's little theorem ( $m = 16$ )

Prior Art

Fig. 2

09900021.112101

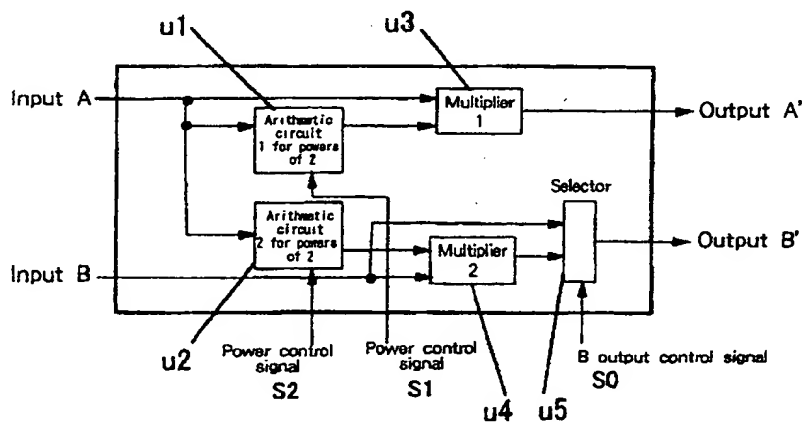


Example calculation process using the algorithm of Itoh and Tsujii ( $m = 16$ )

Prior Art

Fig. 3

Basic arithmetic module



Multiplication device used for the circuit configuration

Fig. 4

06960001-142401